

PATENT NUMBER and ISSUE DATE

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U.S. **UTILITY** Patent Application

	APPL NUM	FILING DATE	CLASS	SUBCLASS	GAU	EXAMINER			
7	10073240	02/13/2002	257		2811	KANG			
**	**APPLICANTS: Takatani Shinichiro; Miki Hiroshi; Kushida Keiko; Fujisaki Yoshihisa;								
A. S.	Torii Kazuyosh	i;							
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; ;	**CONTINUING DATA VERIFIED:								
	THIS APPLICATION IS A DIV OF 09/381,396 09/20/1999								
1	THIS APPLICATION IS A DIV OF 09/381,396 09/20/1999 WHICH IS A 371 OF PCT/JP98/01271 03/24/1998 AND ADDRESS OF THE POST OF THE								
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4 H	** FOREIGN APPLICATIONS VERIFIED: JAPAN 9-074994 03/27/1997								
7 11	JAPAN 9-07499								
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1	oreign priority clair		□ yes			ATTORNEY DOCKET NO			
91	5 USC 119 conditions met □ yes □ no /erified and Acknowledged Examiners's intials				520.37607VX1				
į	TITLE : Semiconductor device and fabrication method thereof								
-	U.S.DEPT. OF COMM./PAT.& TM-PTO-436L(Rev. 12-94)								
新	共產黨 與	的数据的逻辑	W. The	特性技術	4# P				
ΝĘ	NO PORCH STREET	经产品的公司公司	CONTRACTOR DE	经过少少	(See 10 7 10	三、25.7年305.651、原始3周以前的海湾			

NOTICE OF ALLOWANCE MAILED	Assistant Examiner	Total Claims Print Claim for O.G.		
ISSUE FEE Amount Due Date Paid		Sheets Drwg. Figs. Drwg. Print Fig.		
TERMINAL	Primary Examiner PREPARED FOR ISSUE	Application Examiner		
DISCLAMER	WARNING: The information disclosed herein may be restricted. Unauthorized disclosure may be prohibited by the United States Code Title 35, Sections 122, 181 and 368, Possession outside the U.S. Patent & Trademark Office is restricted to authorized employees and contractors only			
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